<u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Daisuke Kawagoe

Examiner: Ishwarbhai Patel

Seal No.:

10/646,478

Group Art Unit: 2841

2510de

August 22, 2003

Docket No.: 884.937US1

Title:

STACKED VIA STRUCTURE THAT INCLUDES A SKIP VIA

Assignee:

**Intel Corporation** 

Customer No: 21186

## **AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This paper responds to the Office Action mailed on January 12, 2005. Please amend the above-identified patent application as follows.